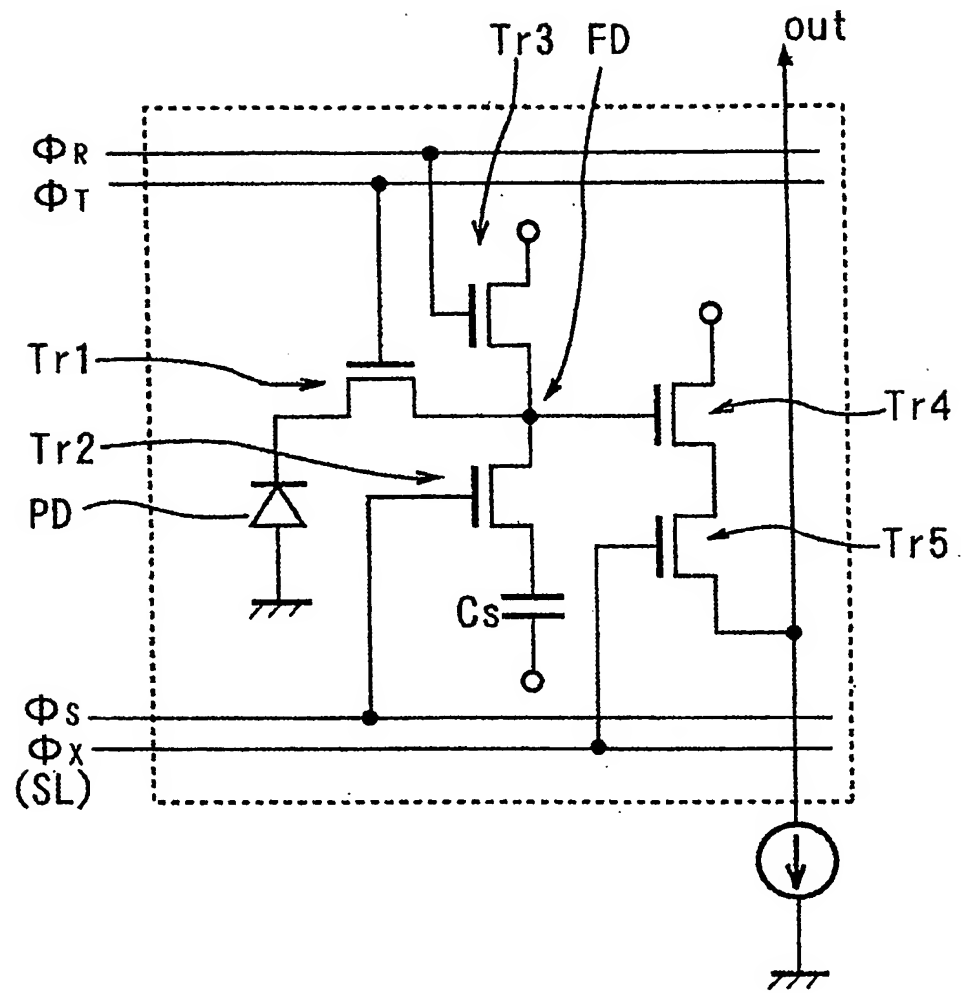
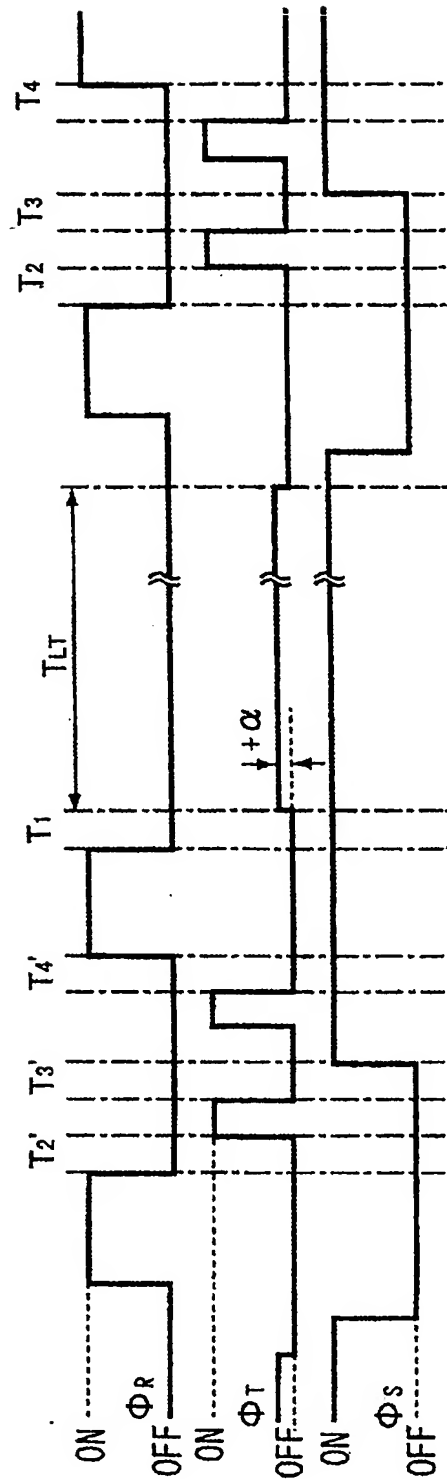


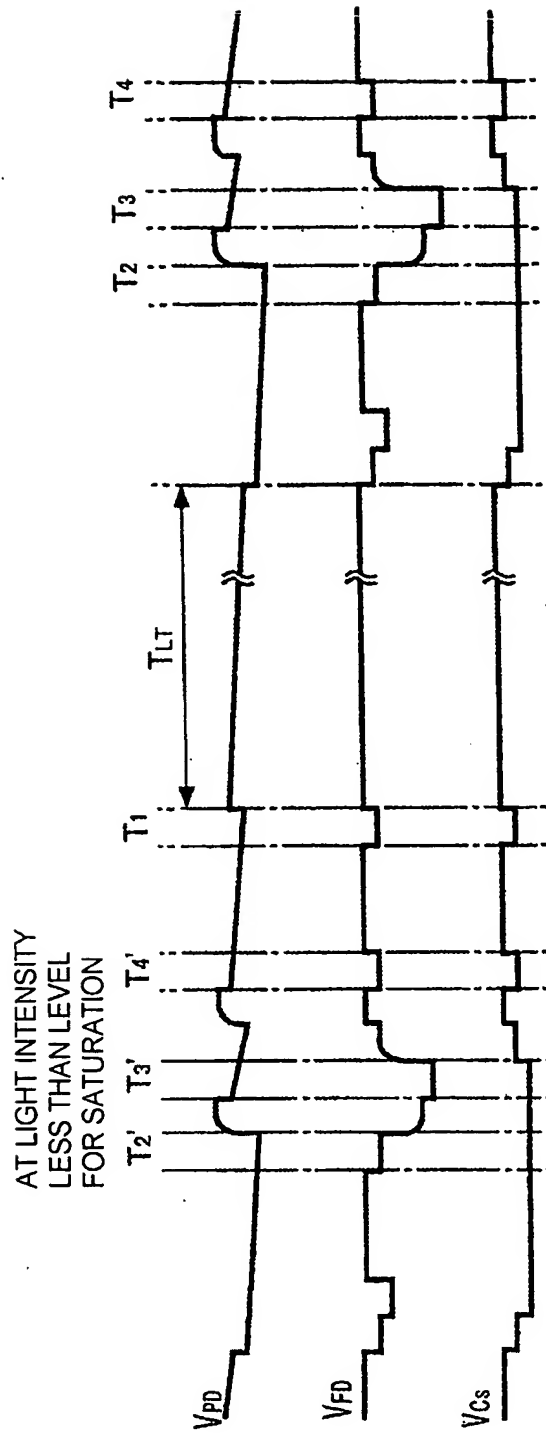
[Fig. 1]



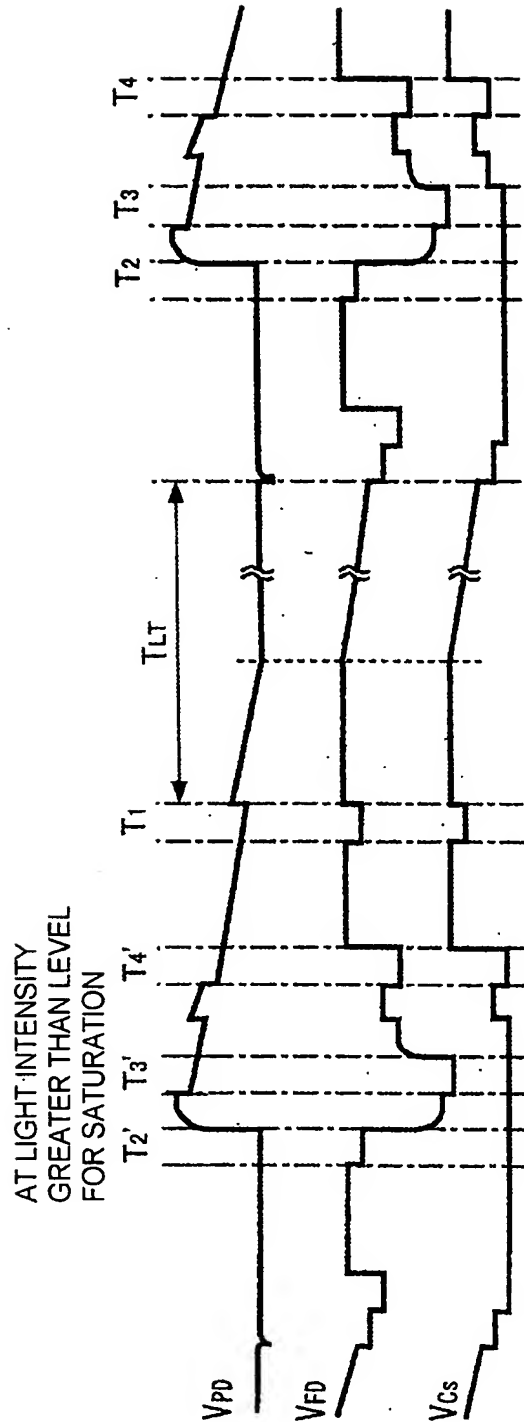
[Fig. 3-1]



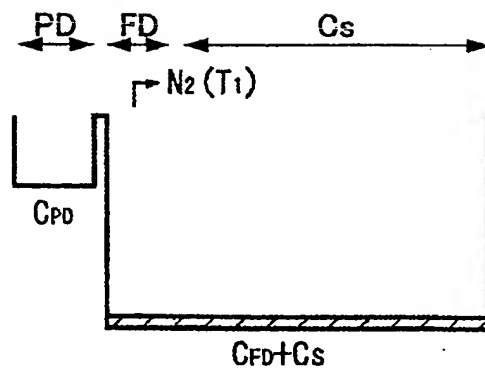
[Fig. 3-2]



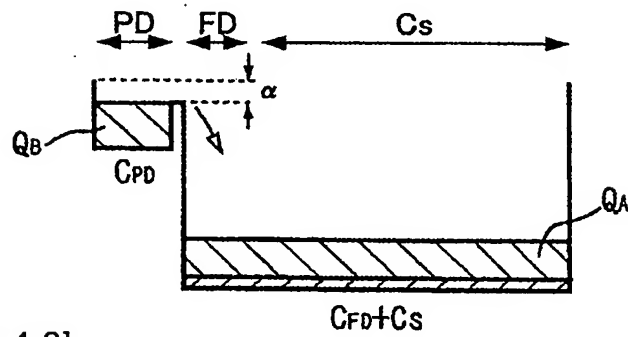
[Fig. 3-3]



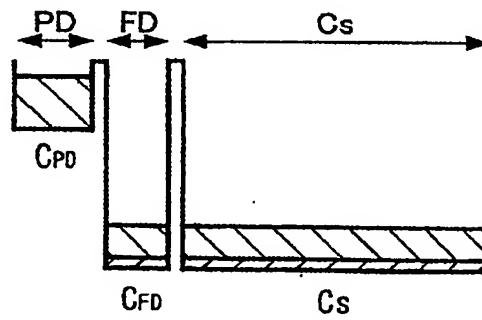
[Fig. 4-1]



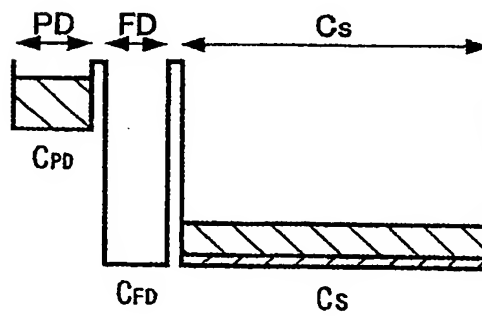
[Fig. 4-2]



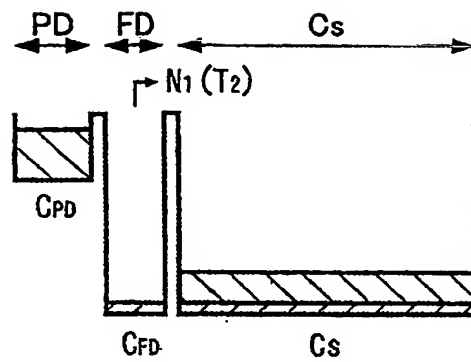
[Fig. 4-3]



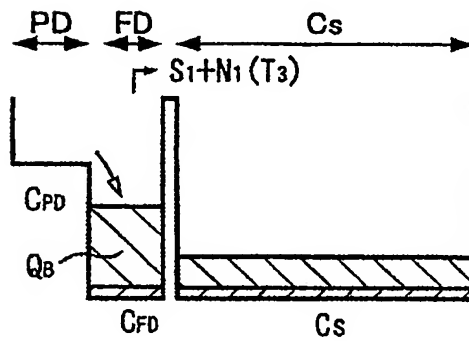
[Fig. 4-4]



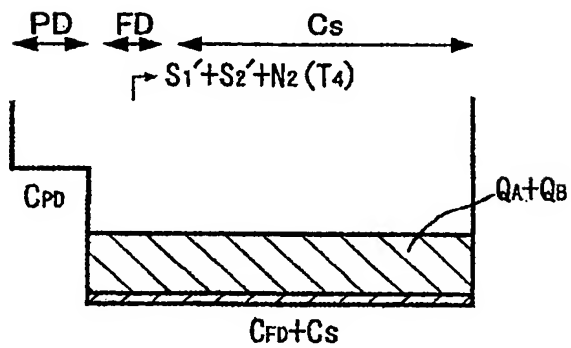
[Fig. 5-1]



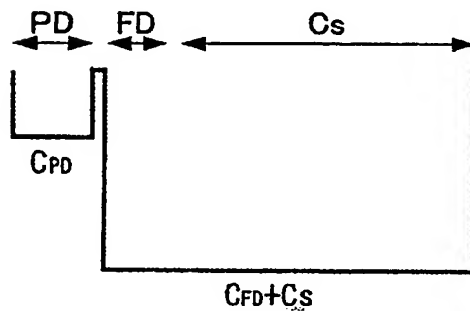
[Fig. 5-2]



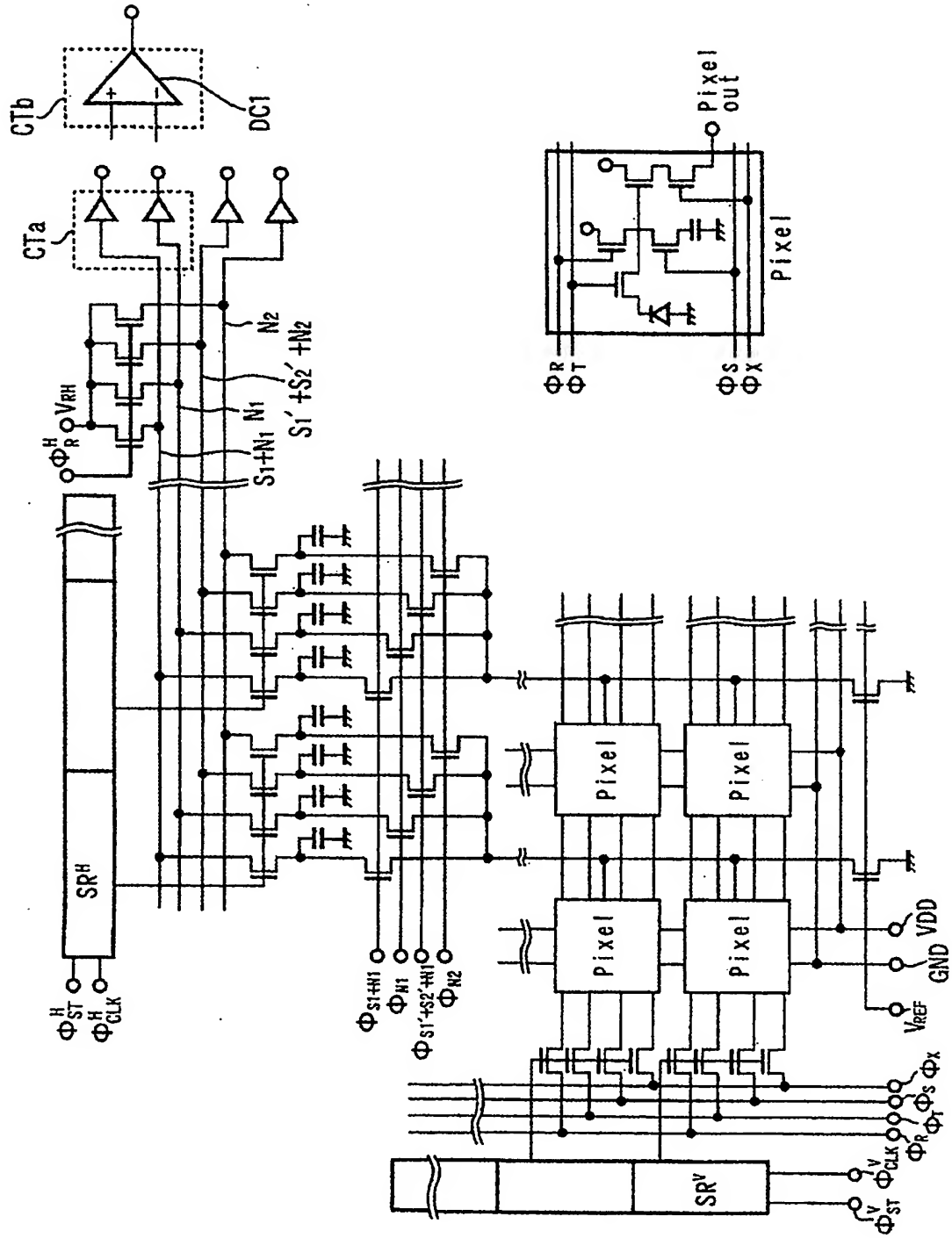
[Fig. 5-3]



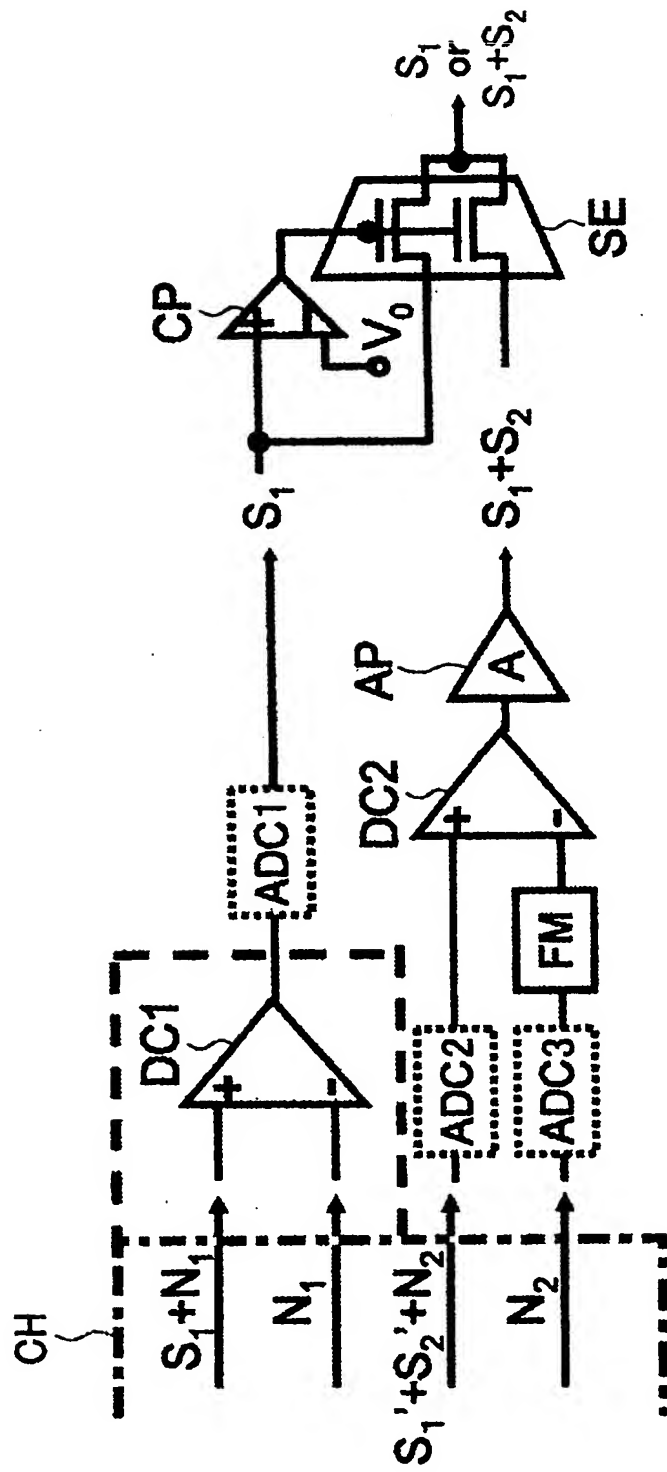
[Fig. 5-4]



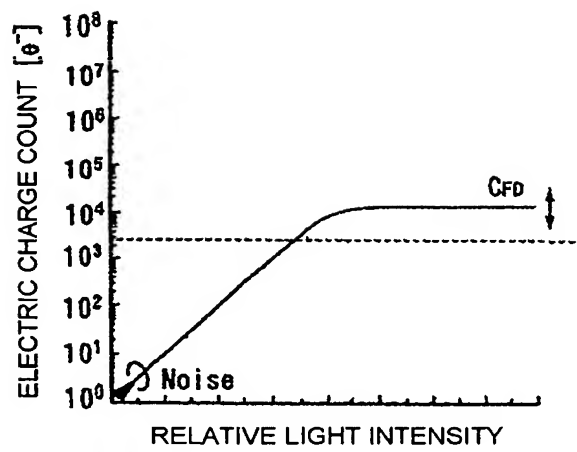
[Fig. 6]



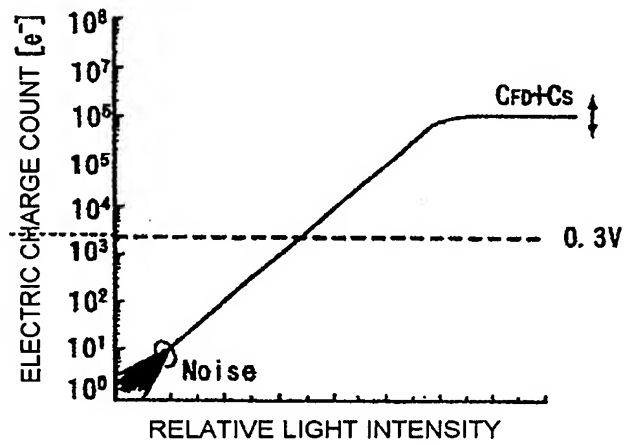
[Fig. 7]



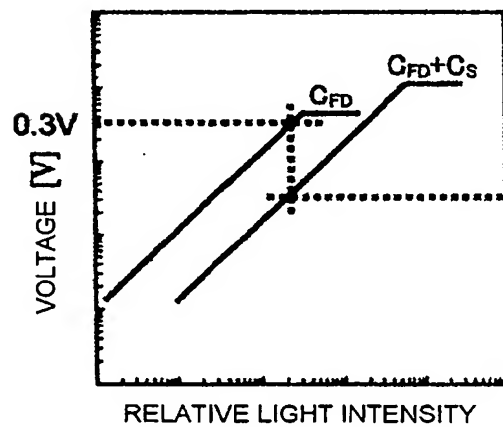
[Fig. 8-1]



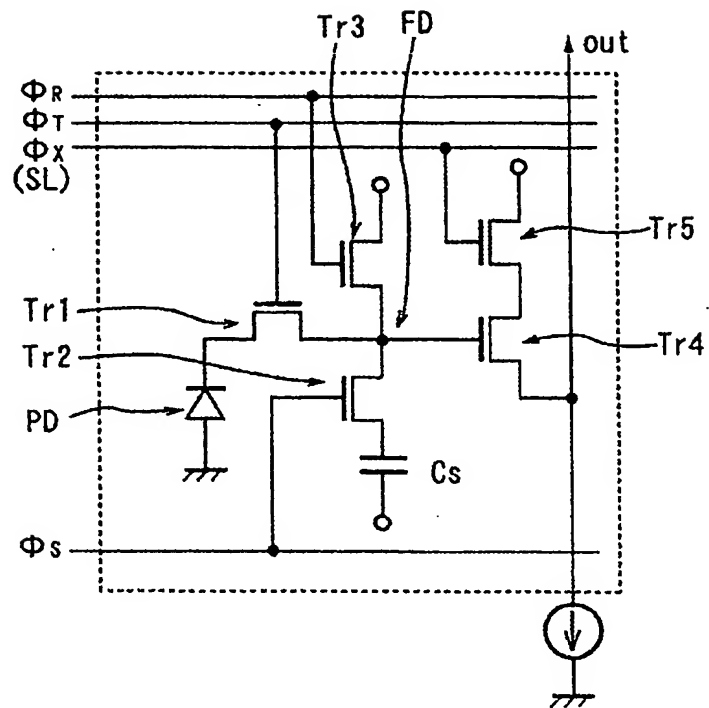
[Fig. 8-2]



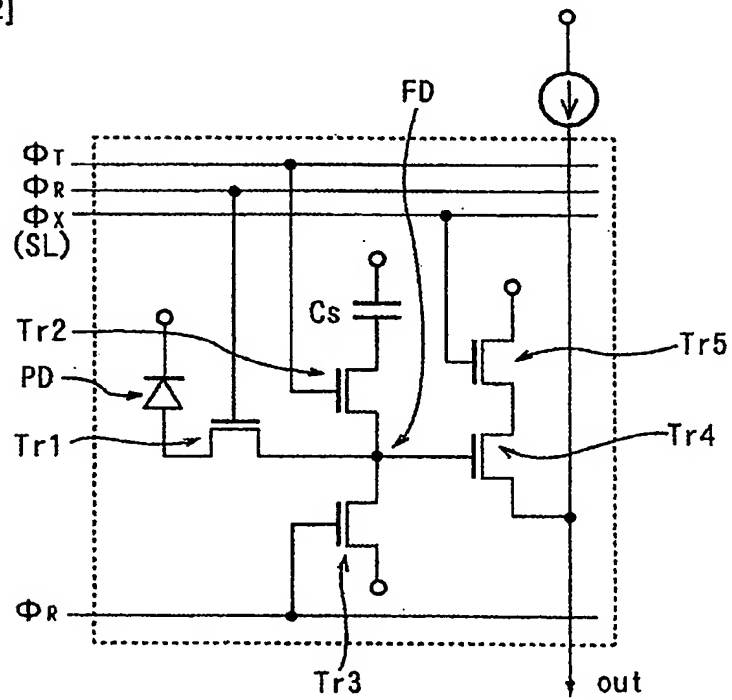
[Fig. 8-3]



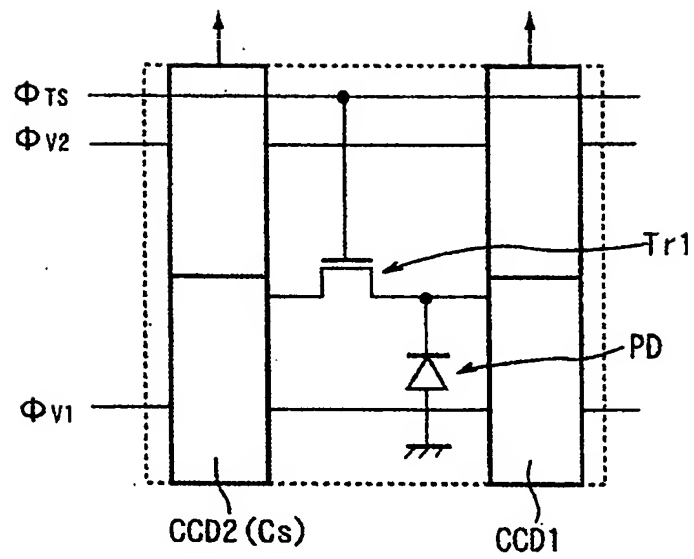
[Fig. 9-1]



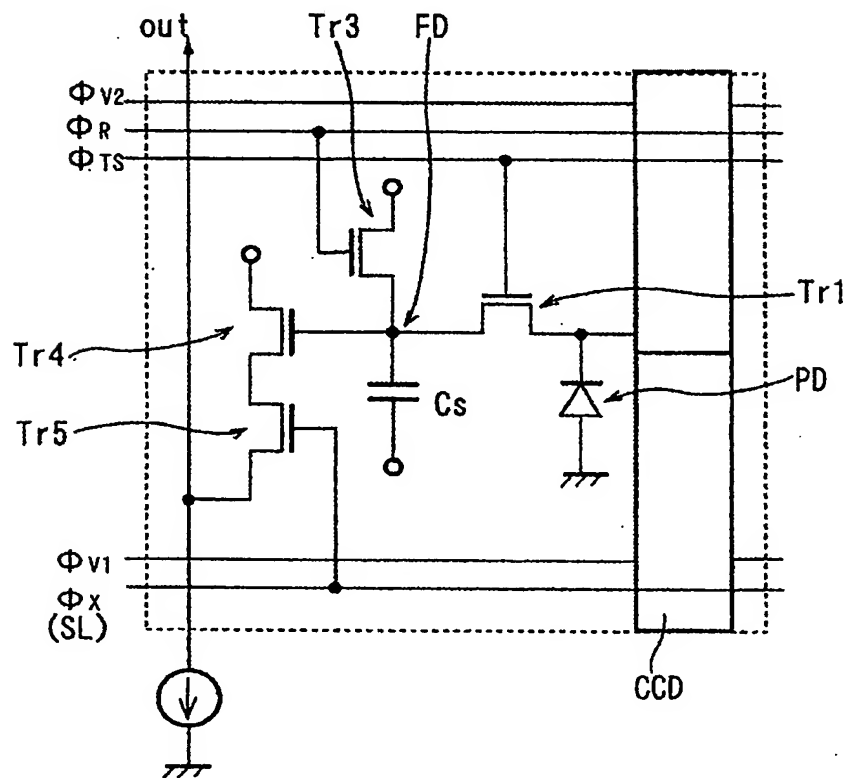
[Fig. 9-2]



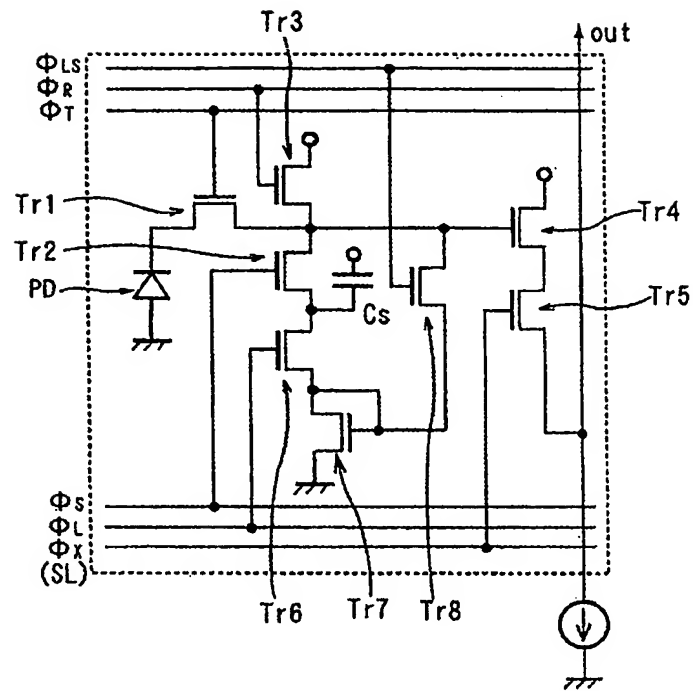
[Fig. 10-1]



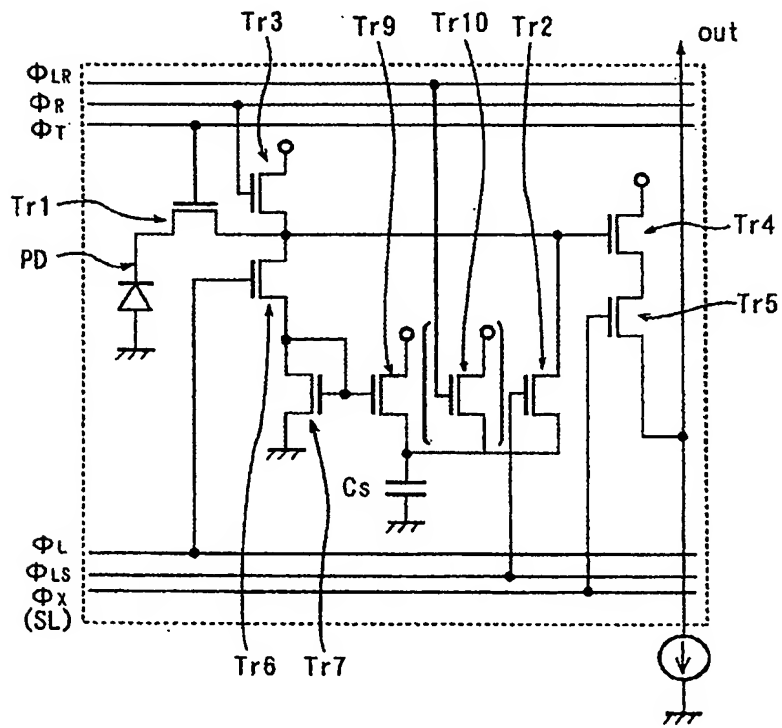
[Fig. 10-2]



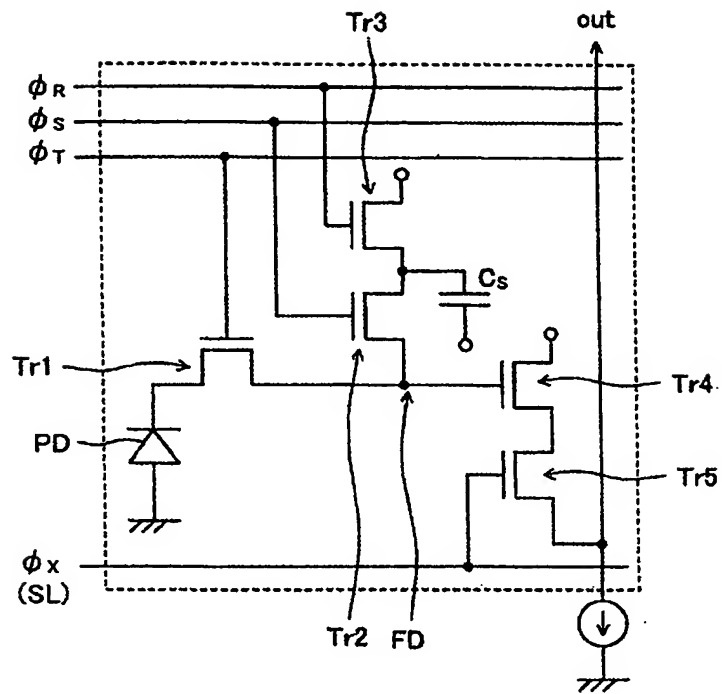
[Fig. 11-1]



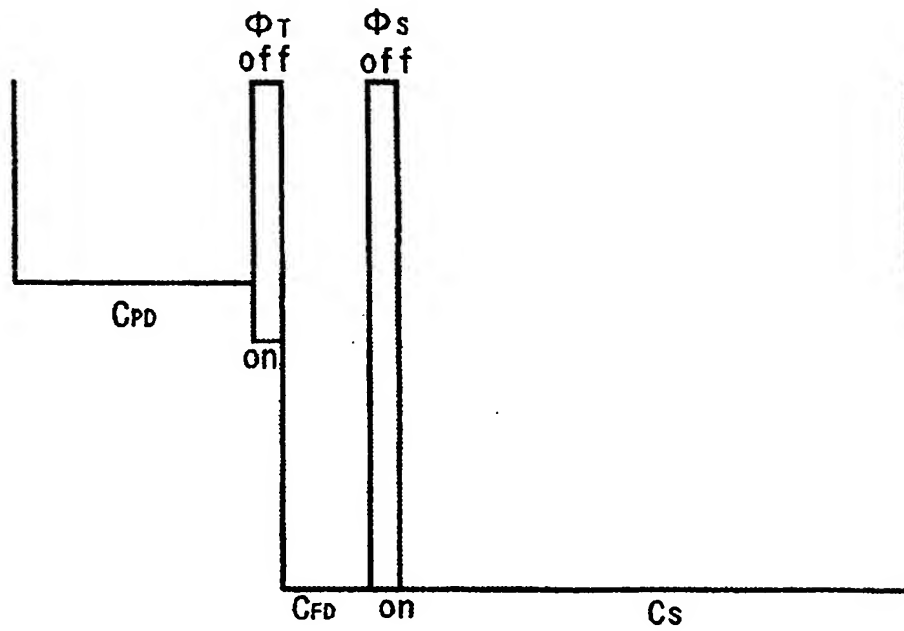
[Fig. 11-2]



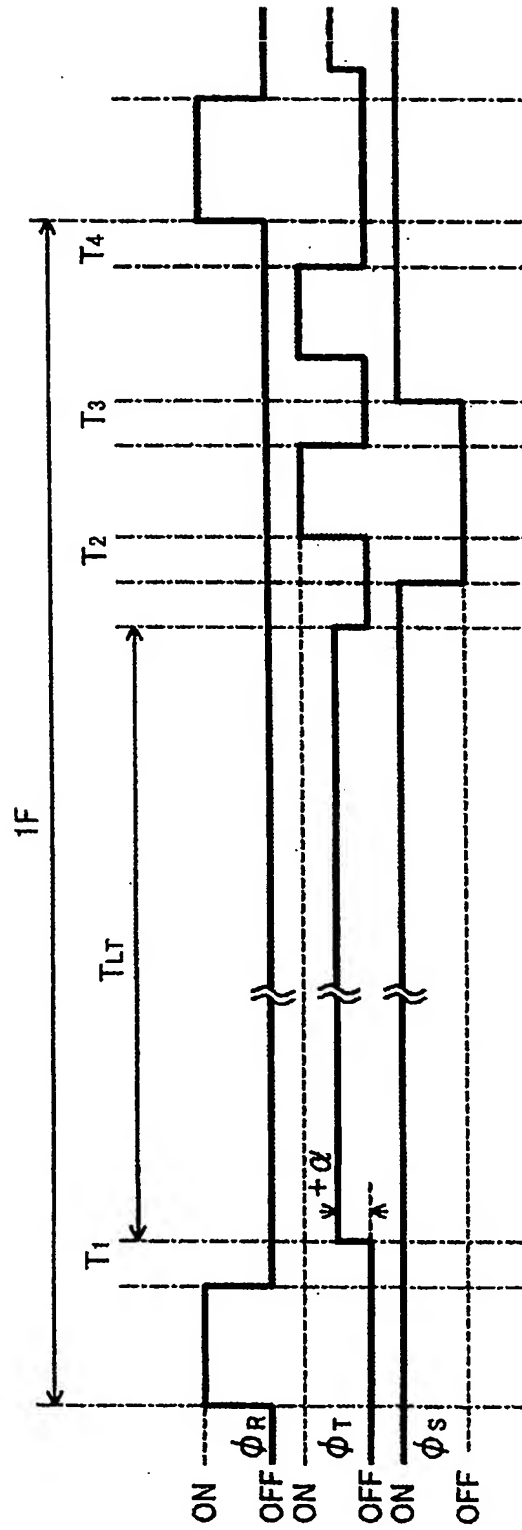
[Fig. 12]



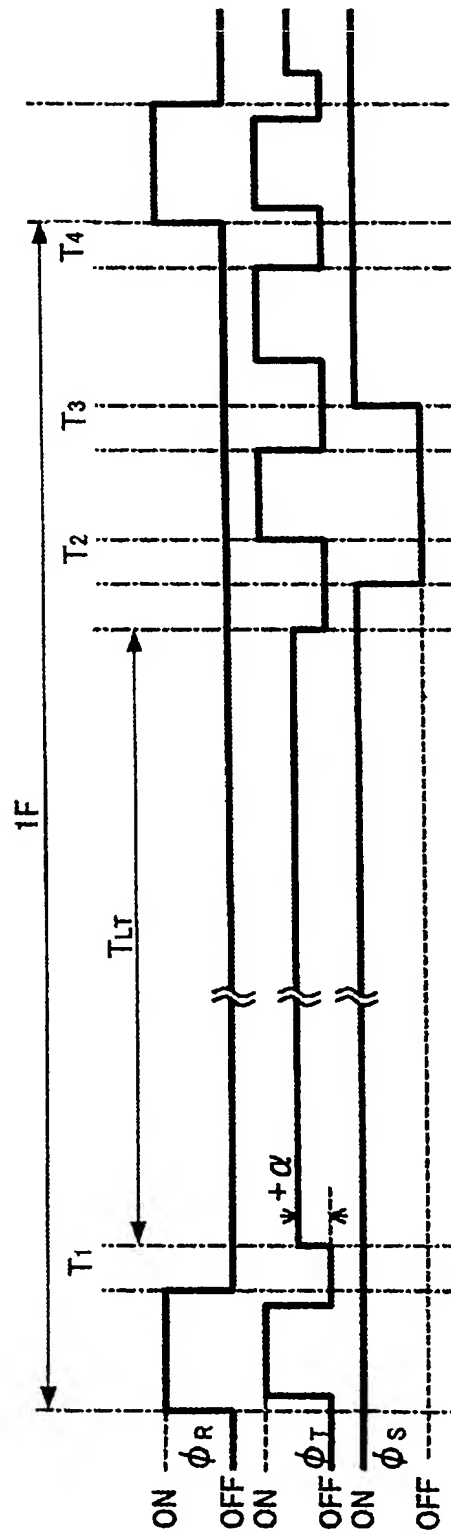
[Fig. 13]



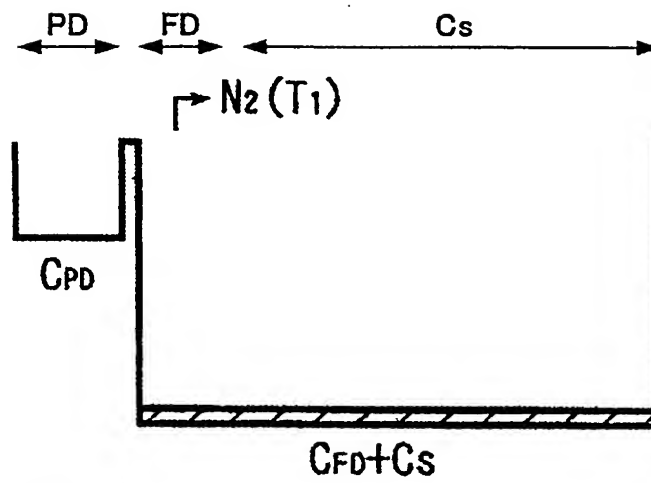
[Fig. 14-1]



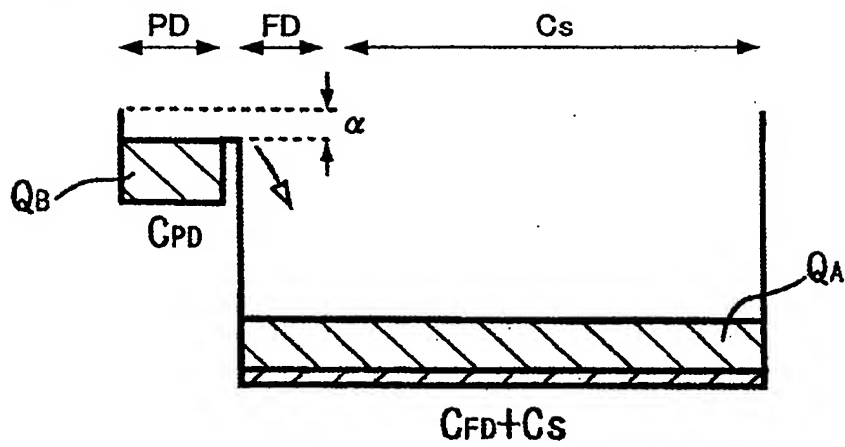
[Fig. 14-2]



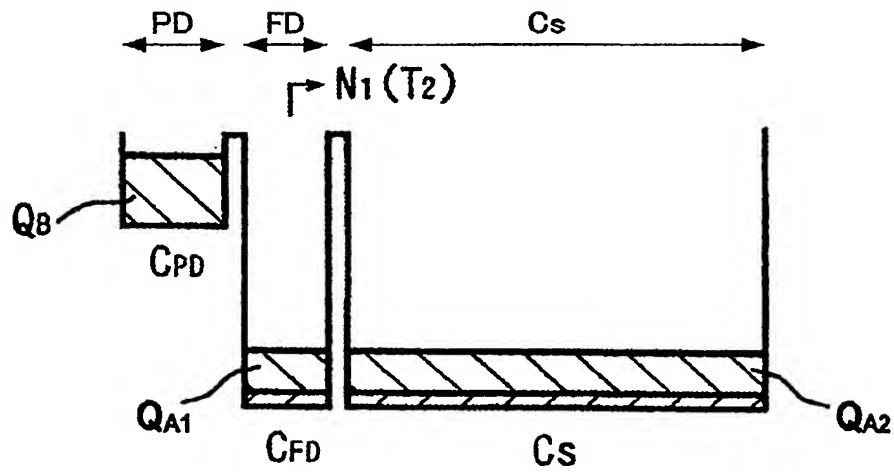
[Fig. 15-1]



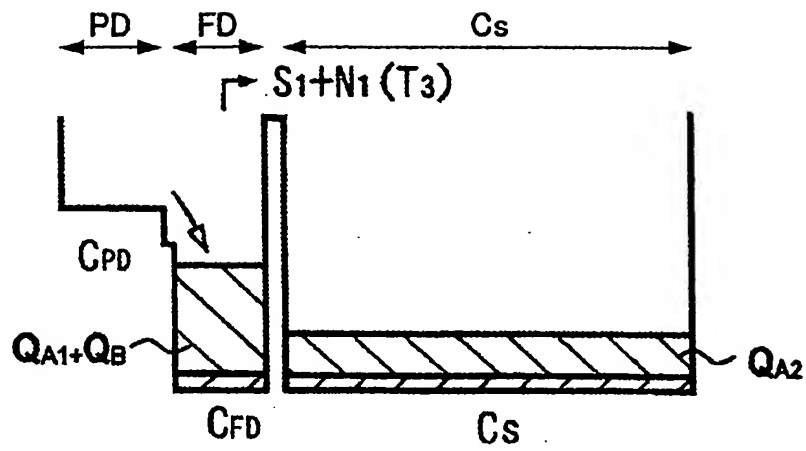
[Fig. 15-2]



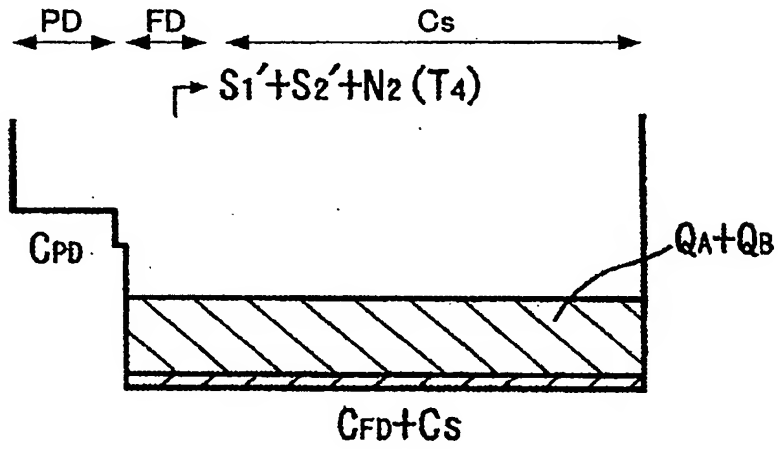
[Fig. 15-3]



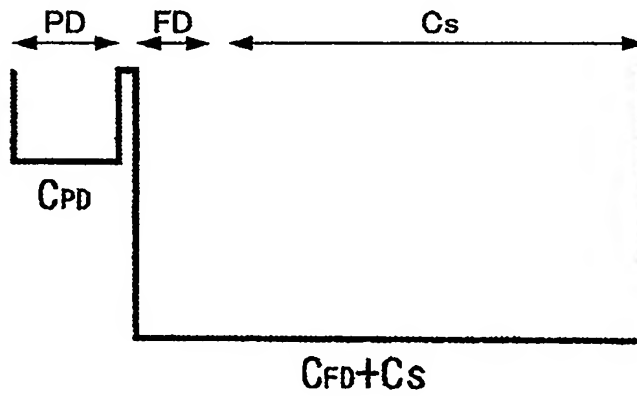
[Fig. 16-1]



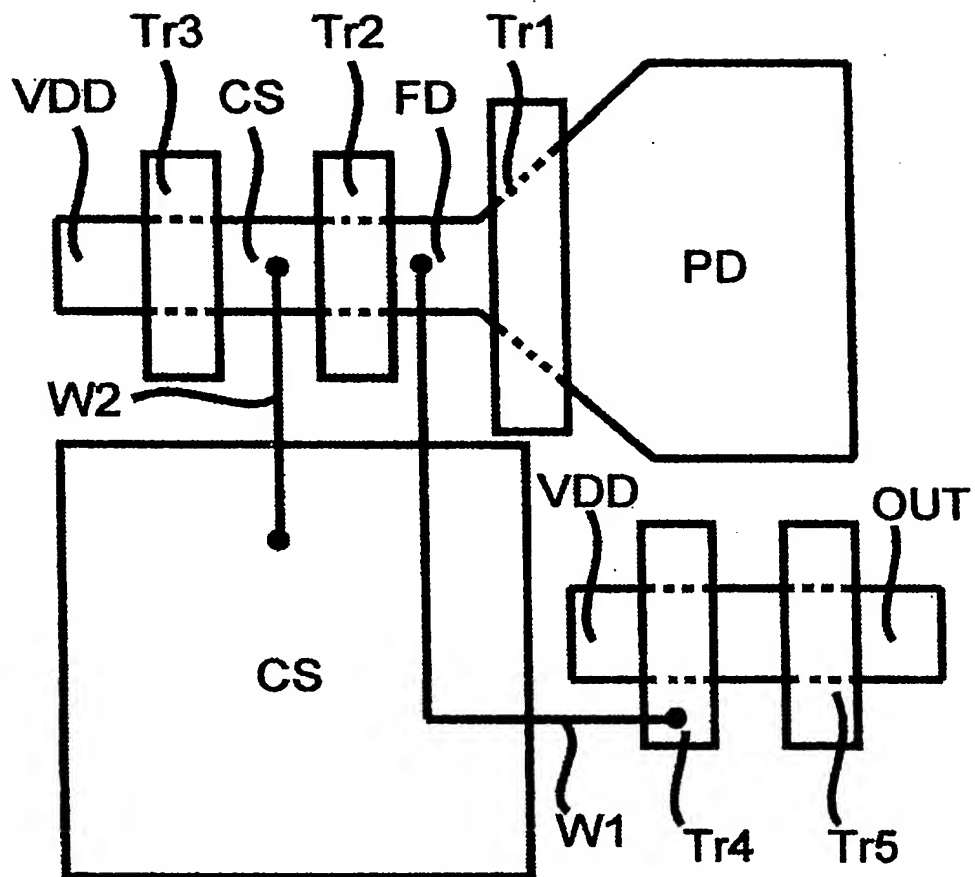
[Fig. 16-2]



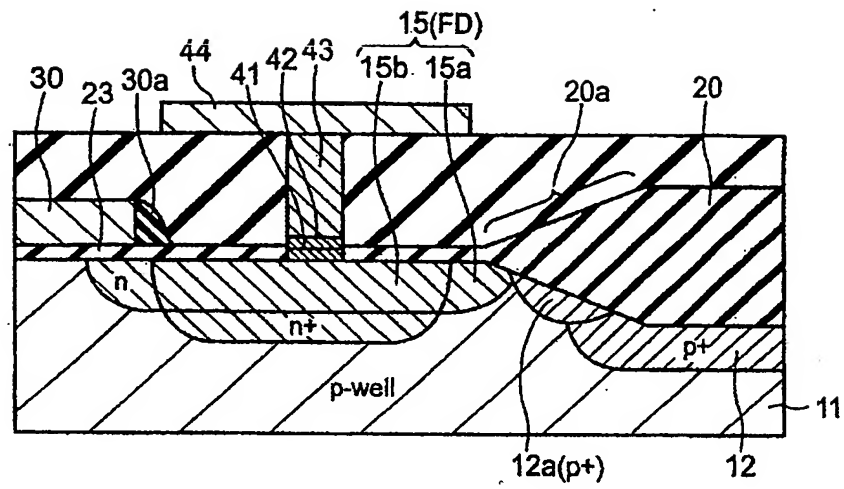
[Fig. 16-3]



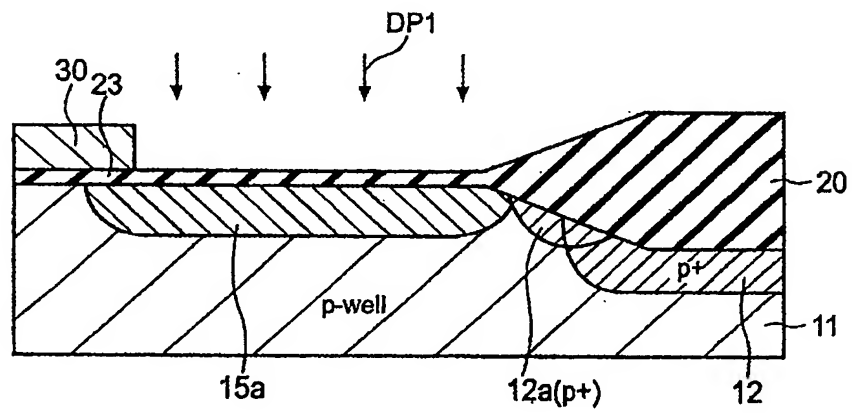
[Fig. 17]



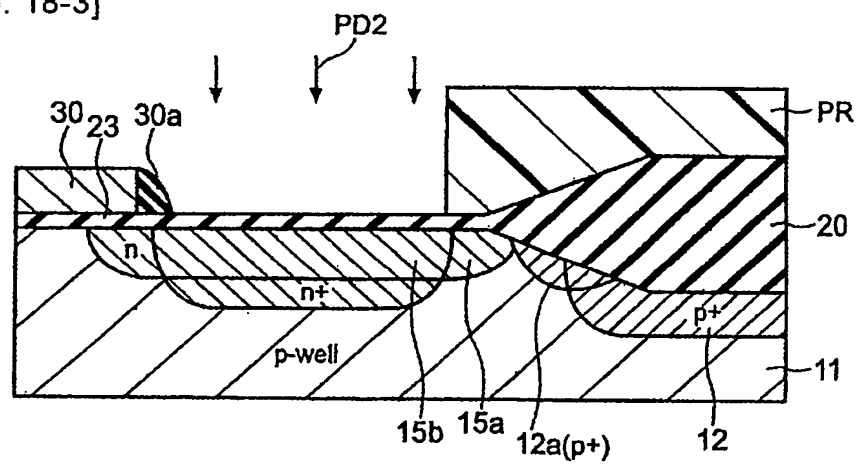
[Fig. 18-1]



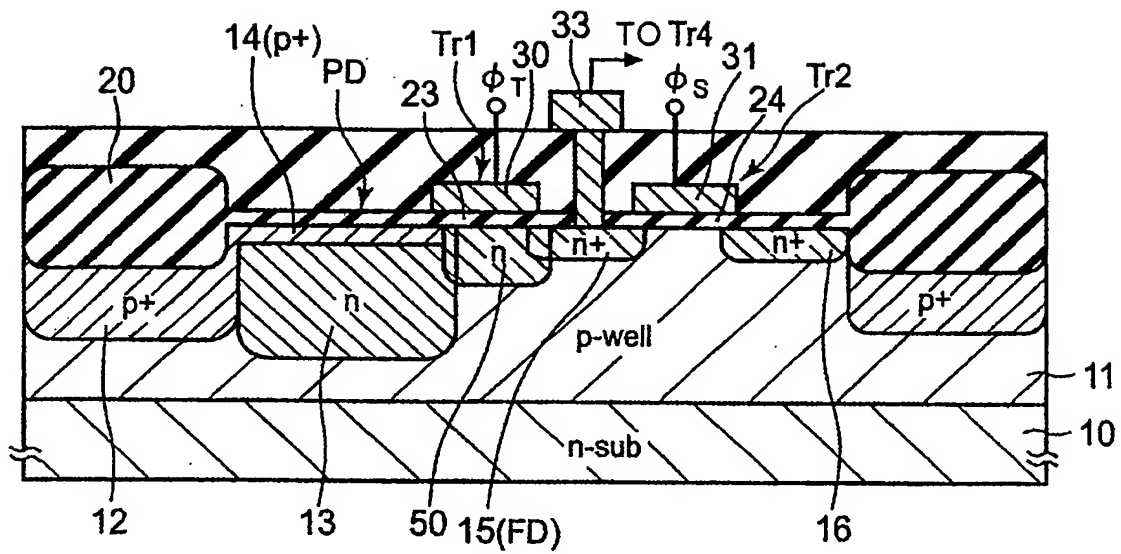
[Fig. 18-2]



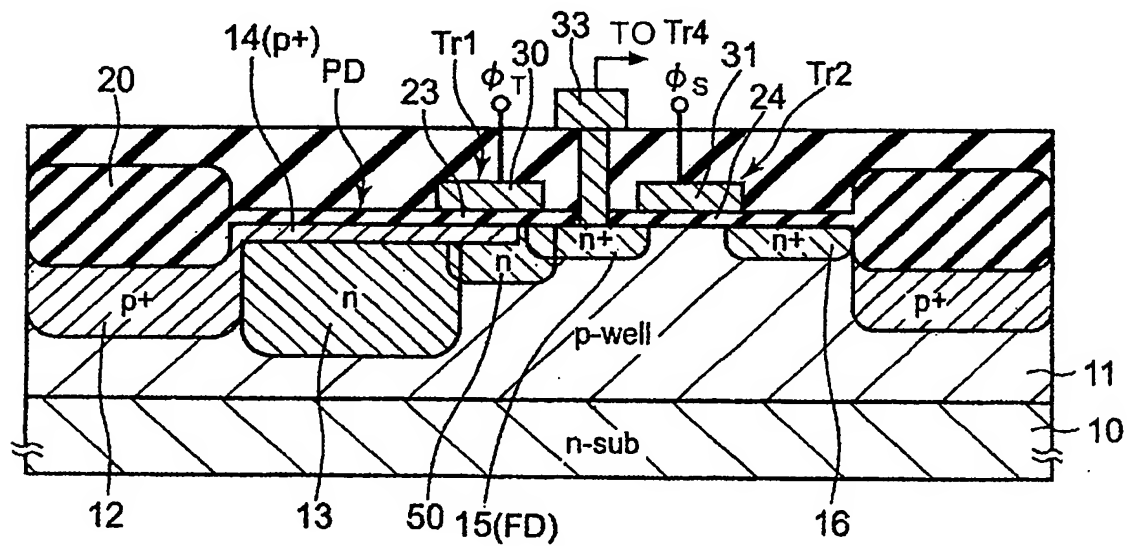
[Fig. 18-3]

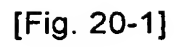


[Fig. 19-1]



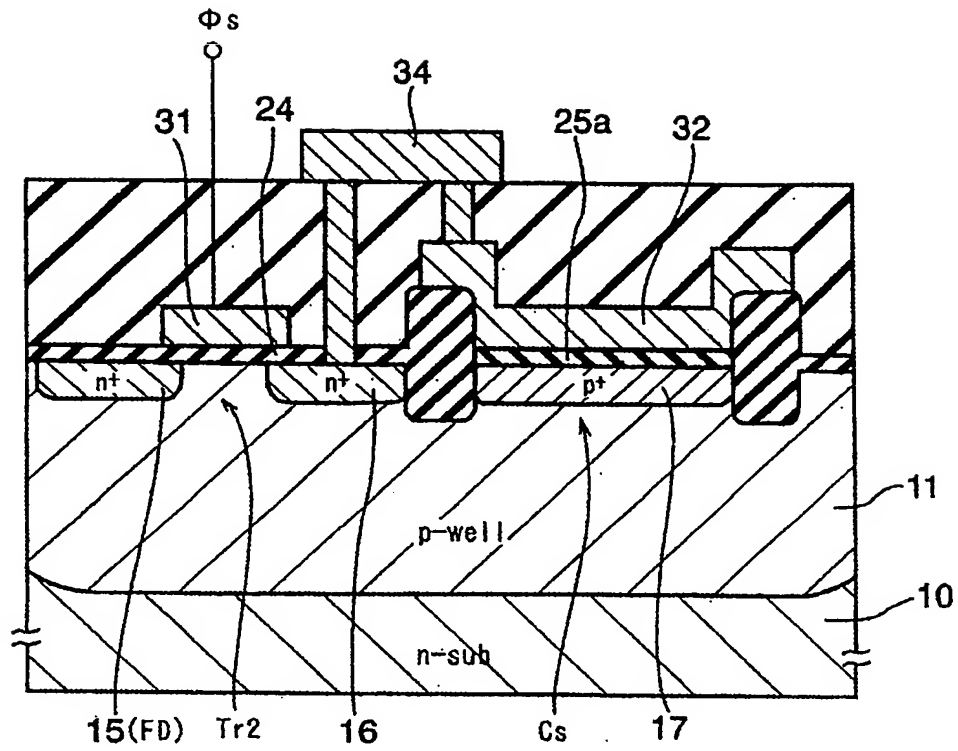
[Fig. 19-2]



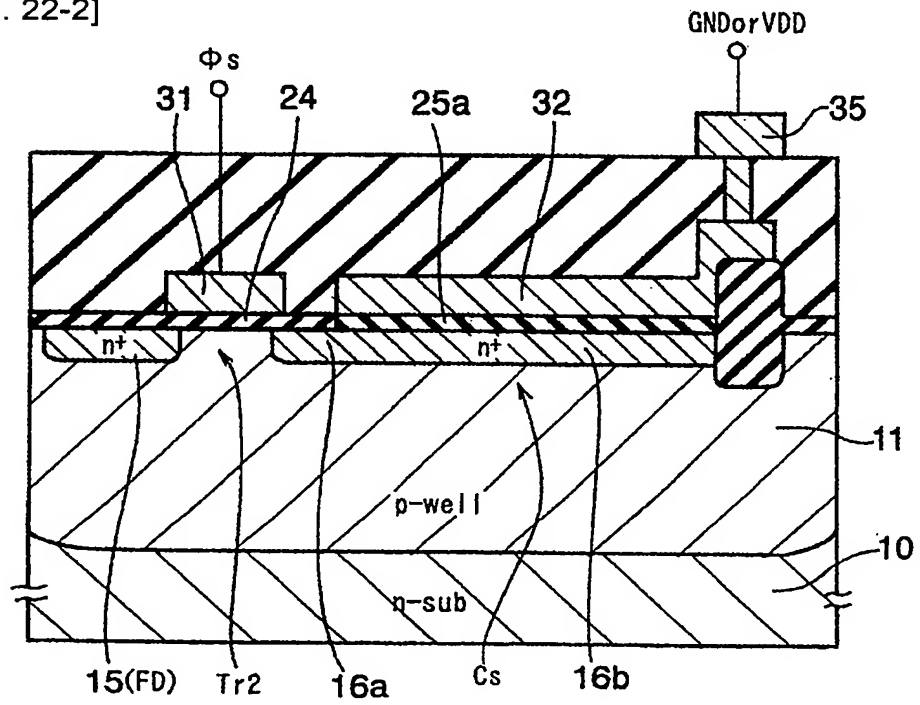


A detailed cross-sectional view of a semiconductor device. The structure is built on an n-substrate (10) which contains a p-well (11). A thin layer 15 (FD) is on the surface. Various regions are doped: n+ (16) and p+ (17). Structures 24, 25, 31, 32, and 34 are shown, along with a gate structure 33. A voltage Φ_s is applied to a contact point. Labels 10, 11, 15 (FD), 16, 17, 24, 25, 31, 32, 33, 34, n-sub, and p-well are present. Arrows indicate electrical connections to the n-substrate and p-well.

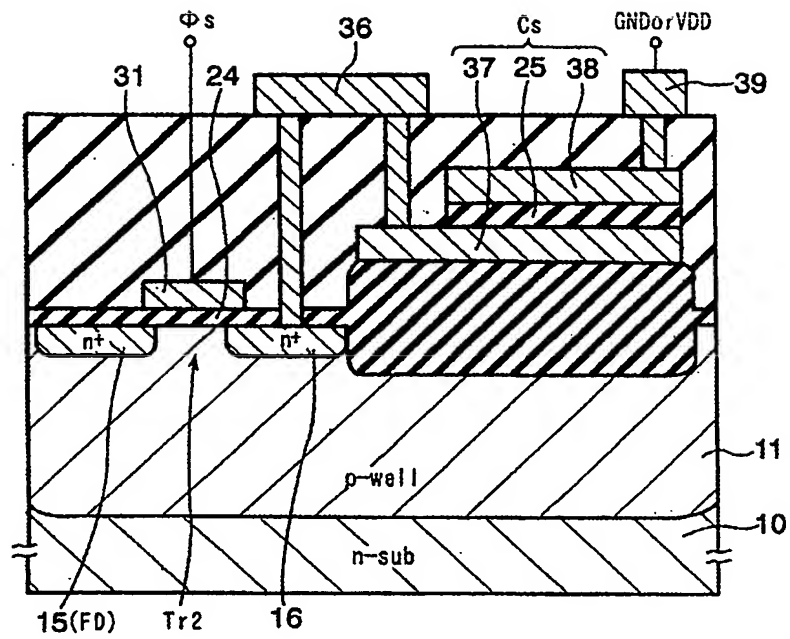
[Fig. 22-1]



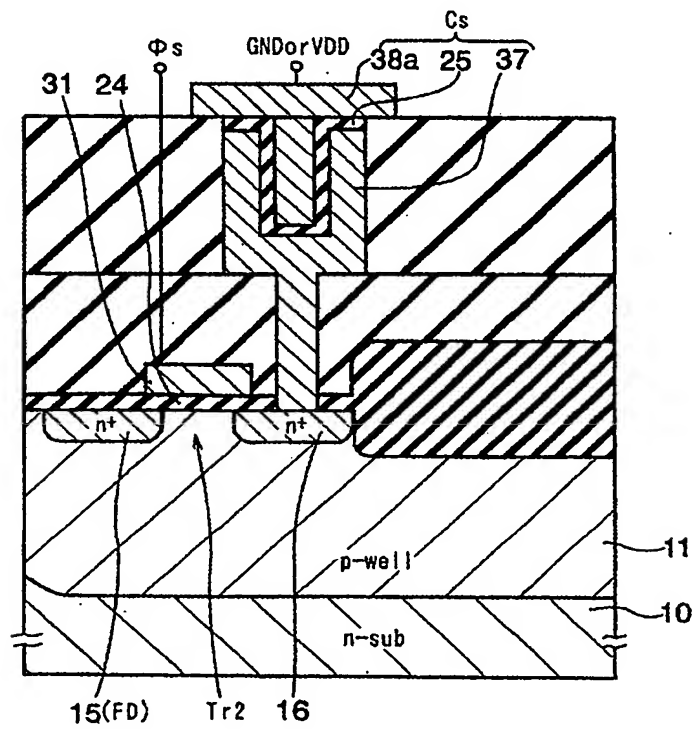
[Fig. 22-2]



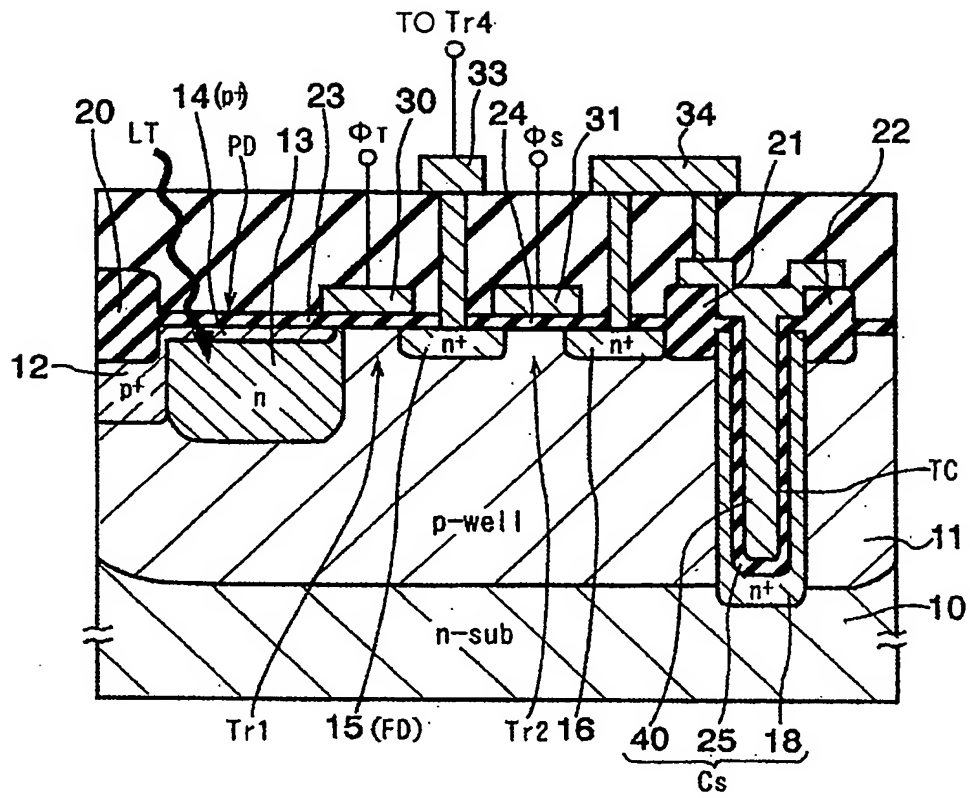
[Fig. 23-1]



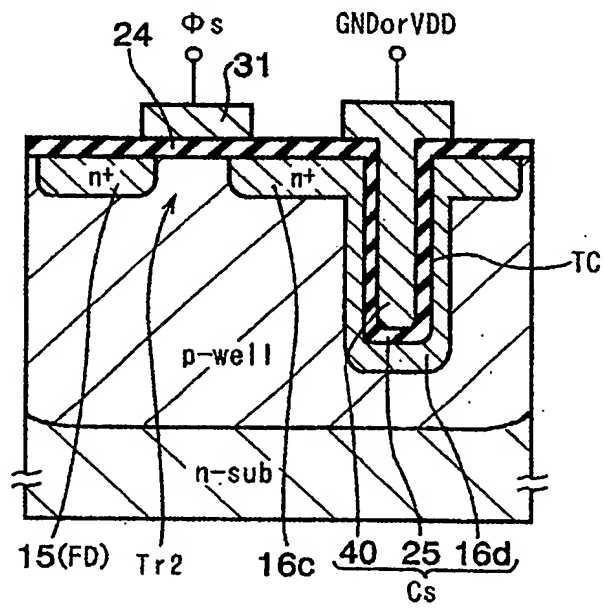
[Fig. 23-2]



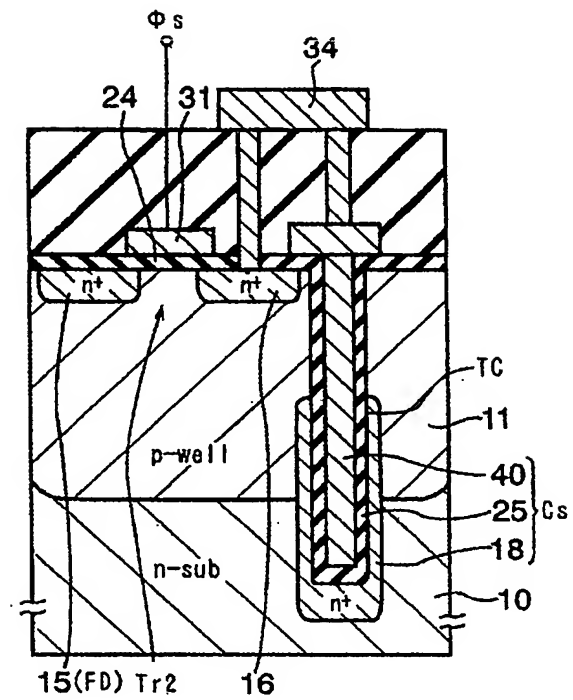
[Fig. 25-1]



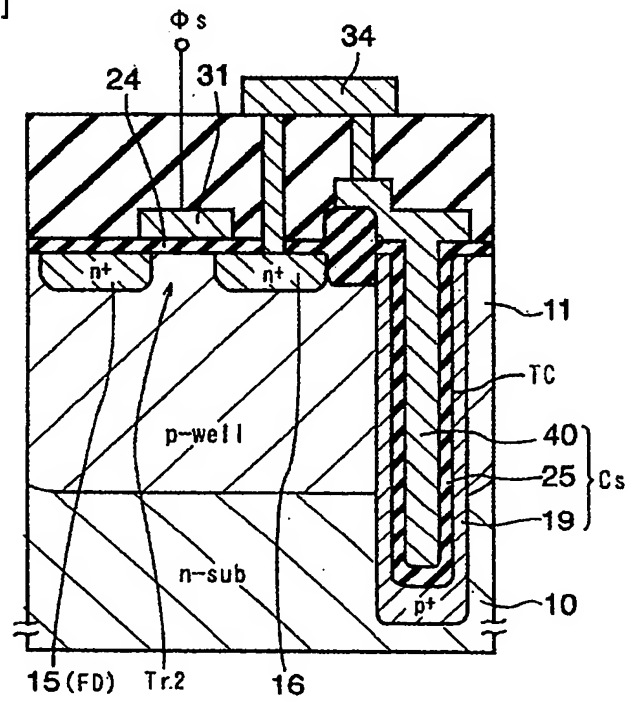
[Fig. 25-2]



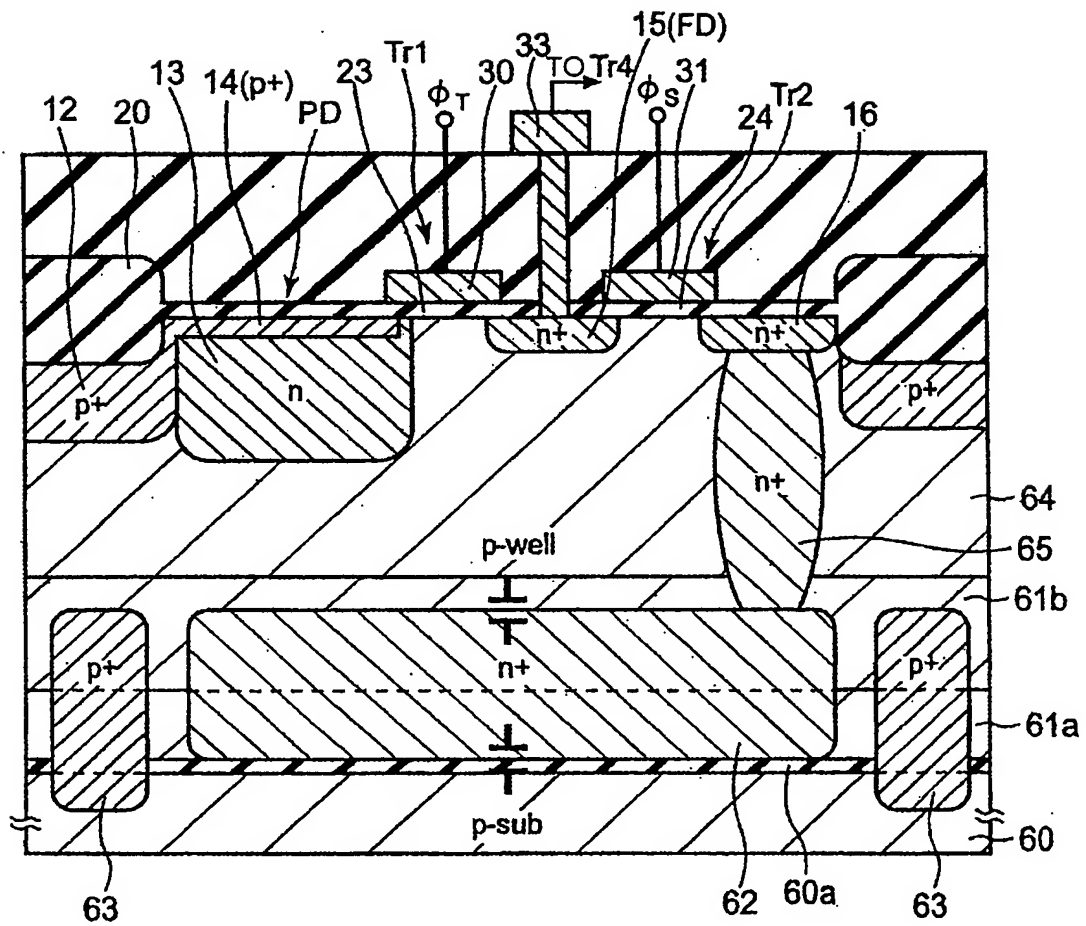
[Fig. 26-1]



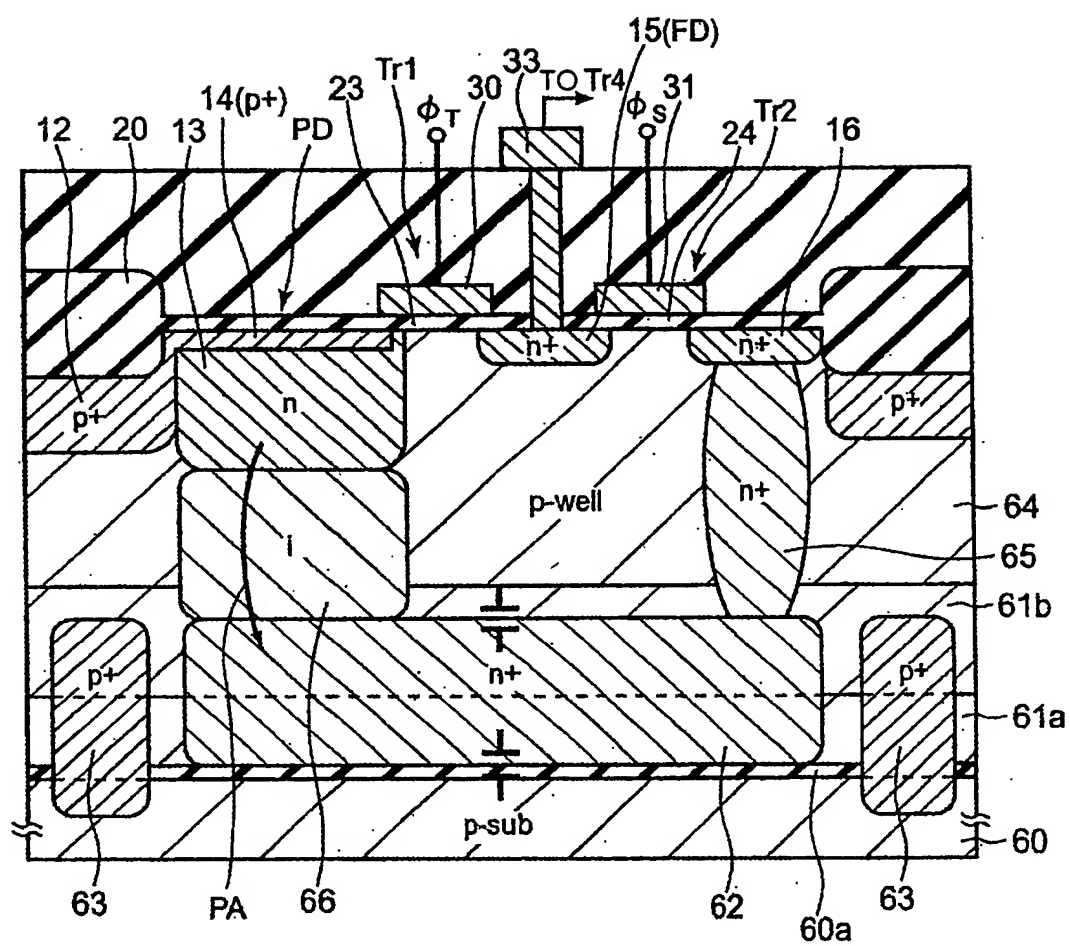
[Fig. 26-2]



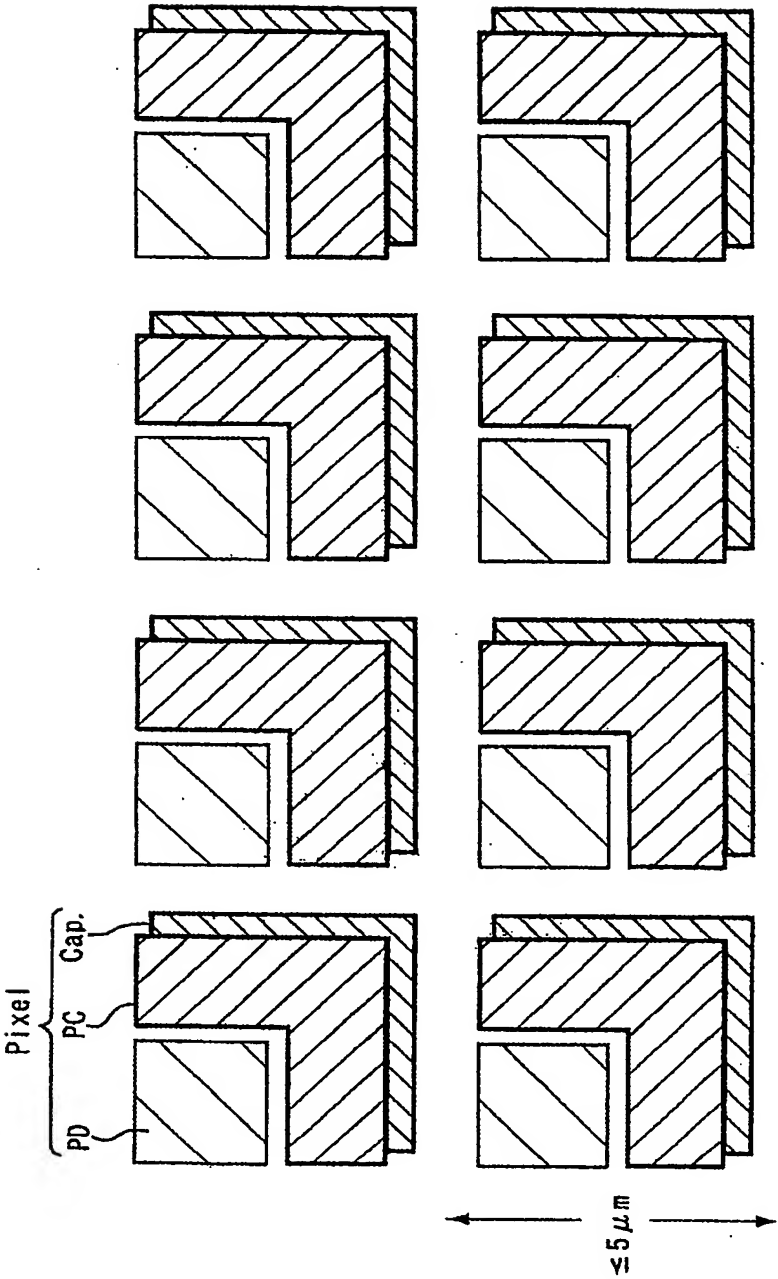
[Fig. 28]



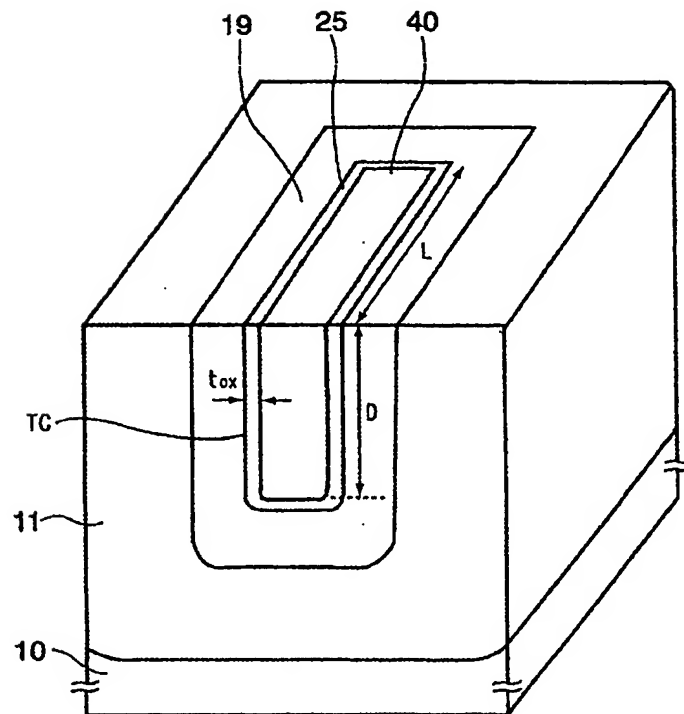
[Fig. 29]



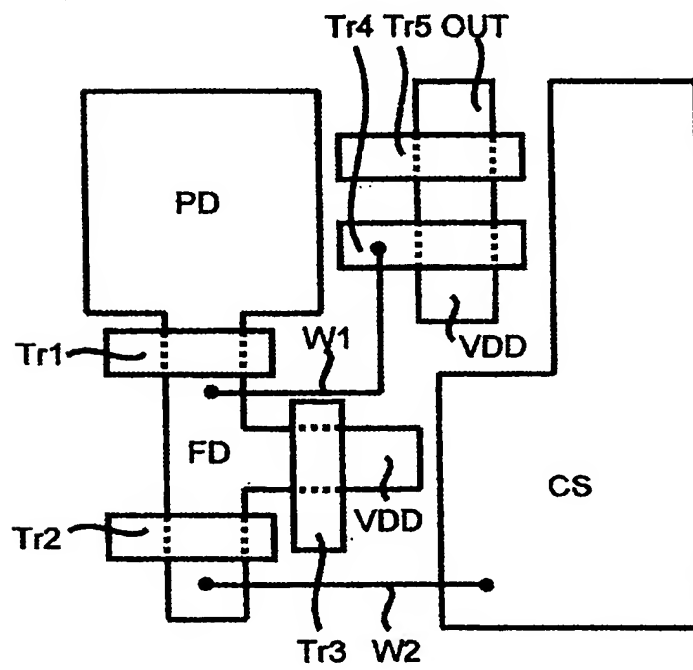
[Fig. 30]



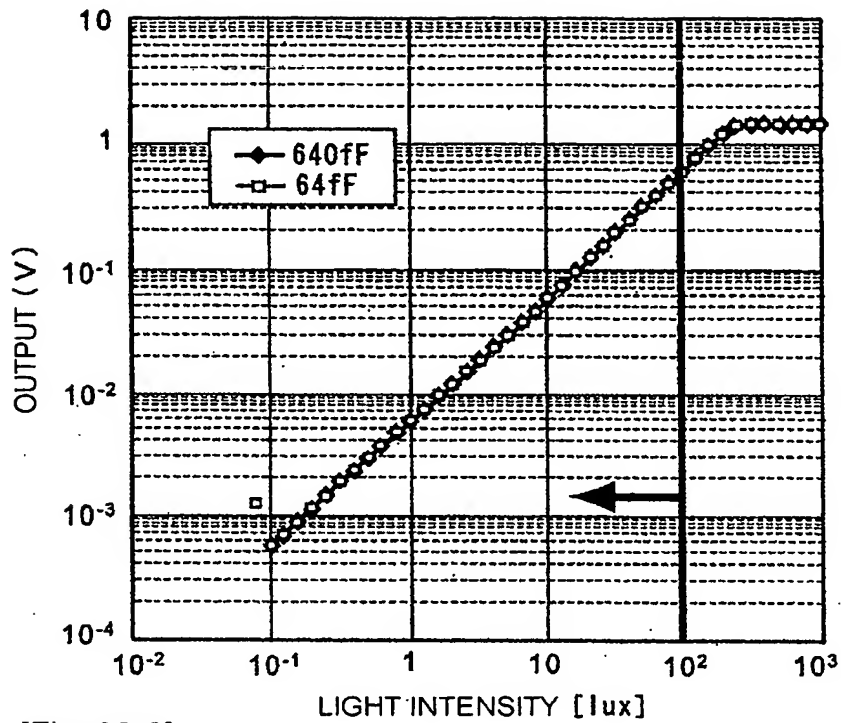
[Fig. 31]



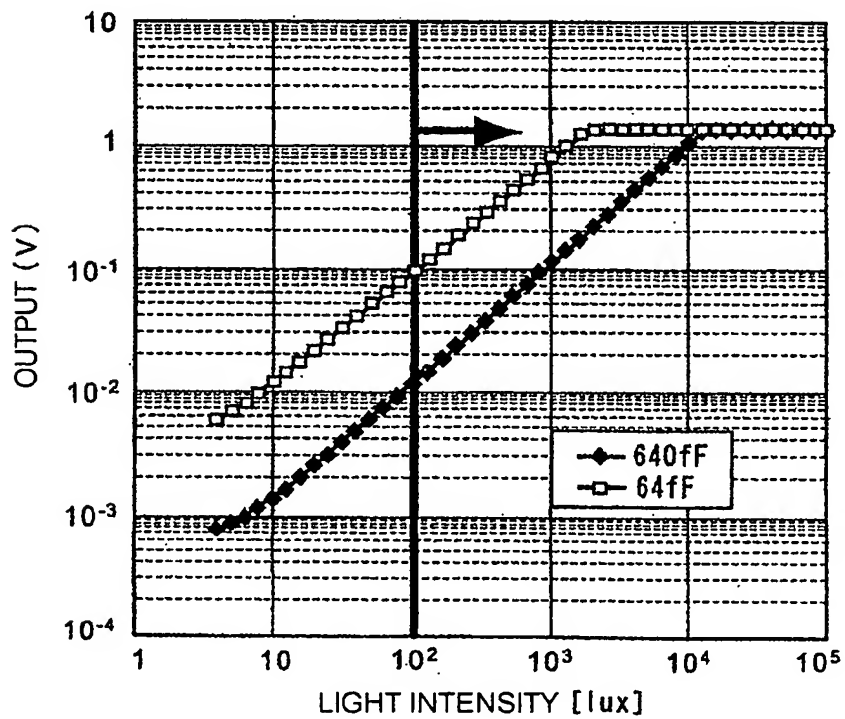
[Fig. 32]



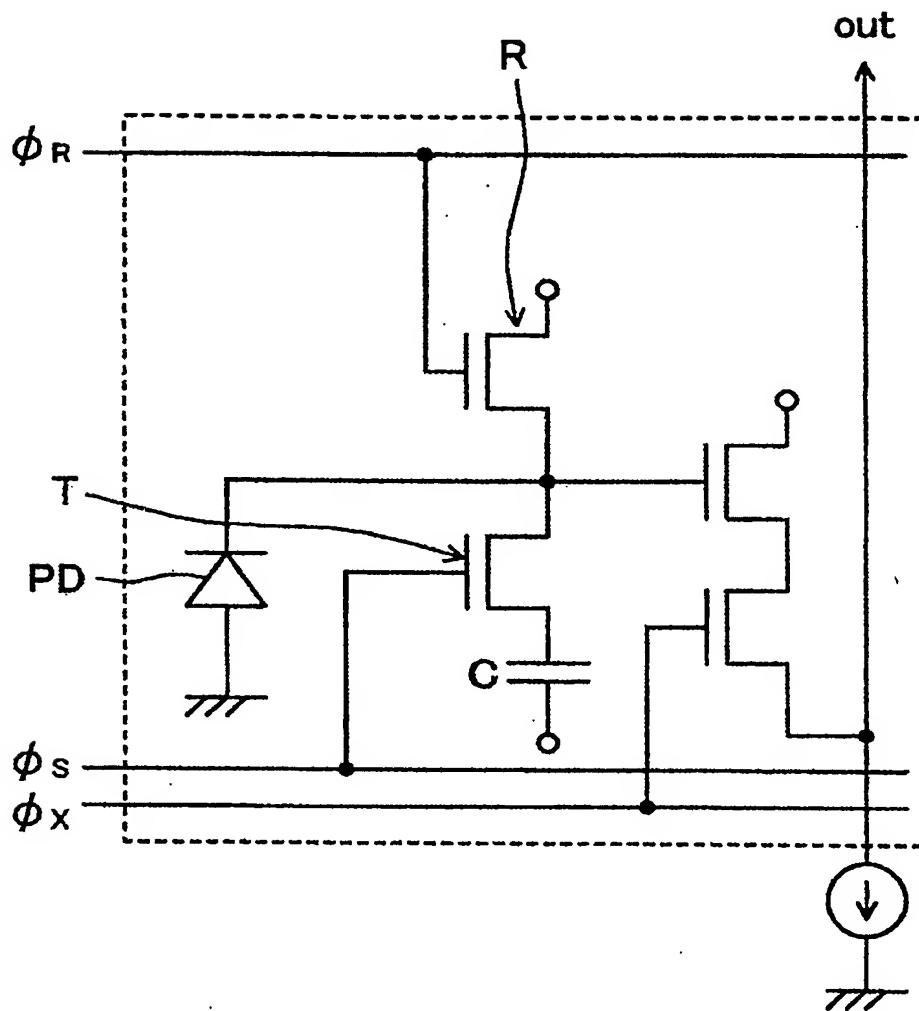
[Fig. 33-1]



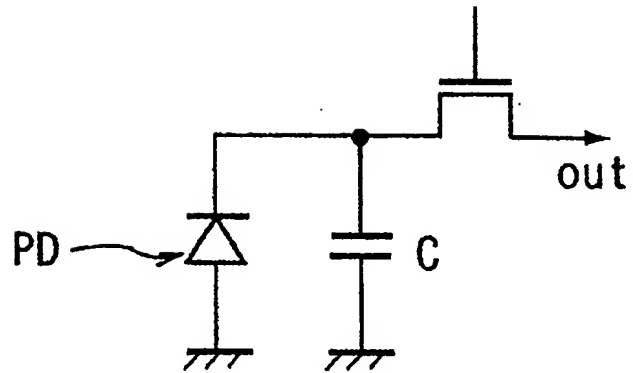
[Fig. 33-2]



[Fig. 36]



[Fig. 37]



[Fig. 38]

